Germany

SIEMENS

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6ES7512-1CK00-0AB0

# SIEMENS PLC SIMATIC S7-1500 CPU 1512C-1 PN 6ES7512-1CK00-0AB0 SIMATIC MEMORY CARD REQUIRED

PLC SIMATIC S7-1500 CPU 1512C-1 PN

## **Basic Information**

- Place of Origin:
- Brand Name:
- CE • Certification:
- Model Number:
- Minimum Order Quantity:
- Price:

VOBOAL

- 15,10 x 15,40 x 4,60 • Packaging Details:
- Delivery Time:
- 10-12Days L/C, T/T • Payment Terms:
- Supply Ability:

# 11.00 10.00

## **Product Specification**

•	Сри Туре:	S7-1518-4 PN/DP
•	Memory:	4 MB
•	Weight:	0.5 Kg
•	Number Of Digital Outputs:	32
•	Certifications:	CE, UL, CSA, FM, CCC
•	Number Of Analog Outputs	:8
•	Operating Temperature:	-20 To 60 Degrees Celsius
•	Number Of Digital Inputs:	32
•	Dimensions:	130 X 125 X 130 Mm
•	Number Of Analog Inputs:	8
•	Protection Class:	IP20
•	Power Supply:	24 V DC



### Product Introduction:

The SIEMENS PLC SIMATIC S7-1500 CPU 1512-1 CK 6ES7512-1CK00-0AB0 is a central processing unit (CPU) specifically designed for industrial automation applications. It is part of the Siemens SIMATIC S7-1500 series, known for its advanced functionality, high performance, and reliability.

Product Information and Specifications:

- Model: CPU 1512-1 CK 6ES7512-1CK00-0AB0

The CPU 1512-1 CK features a powerful processor that ensures fast and efficient execution of control programs. It supports multiple programming languages, including ladder logic, function blocks, and structured text, providing flexibility and ease of use for complex control tasks.

In terms of memory capacity, the CPU 1512-1 CK offers sufficient storage space for both program and data. While specific details were not provided in the query, typical configurations of the CPU include program memory ranging from 150 KB to 500 KB and data memory ranging from 150 KB to 500 KB. This memory capacity allows users to store their control programs and necessary data structures for the PLC's operation.

Designed to operate in demanding industrial environments, the CPU 1512-1 CK delivers reliable and precise control for applications such as manufacturing, process control, and machine automation. It supports a wide range of communication interfaces, enabling seamless integration with other devices and systems within the automation network.

The CPU 1512-1 CK is typically programmed and configured using Siemens' TIA Portal (Totally Integrated Automation Portal) software. The TIA Portal provides a comprehensive engineering environment for efficient programming, simulation, and diagnostics, ensuring easy development and maintenance of automation projects. Product Attributes:

- Model: CPU 1512-1 CK 6ES7512-1CK00-0AB0
- Processor: Powerful processor for fast and efficient control program execution
- Programming Languages: Supports ladder logic, function blocks, and structured text
- Memory Capacity: Sufficient storage space for program and data
- Communication Interfaces: Supports various communication interfaces
- Engineering Software: Programmed and configured using Siemens' TIA Portal
- Suitable for: Manufacturing, process control, and machine automation applications

In summary, the SIEMENS PLC SIMATIC S7-1500 CPU 1512-1 CK 6ES7512-1CK00-0AB0 is a reliable CPU with advanced features, sufficient memory capacity, and seamless communication capabilities. It provides efficient and precise control for various industrial processes, making it suitable for a wide range of industrial automation applications.

General information			
Product type	CPU 1512C-1 PN		
designation	GF0 1512G-1 FN		
HW functional	F\$03		
status			
Firmware version	V2.9		
Product function			
<ul> <li>I&amp;M data</li> </ul>	Yes; I&M0 to I&M3		
<ul> <li>Isochronous</li> </ul>	Ves: With minimum OB 6x cycle of 625 us (distributed)		
mode			
Engineering with			
STEP 7 TIA			
Portal	V17 (FW V2.9) / V13 SP1 Update 4 (FW V1.8) or higher		
configurable/integra			
ted from version			
Configuration contro			
via dataset	Yes		
Display			
Screen diagonal	0.45		
[cm]	3.45 cm		
Control elements			
Number of keys	6		
Mode selector	5		
switch	1		
Supply voltage			
Bated value (DC)	24 V		
narmissible range			
lower limit (DC)	19.2 V; 20.4 V DC, for supplying the digital inputs/outputs		
upper limit (DC)	28.8 V		
Reverse polarity			
protection	Yes		
Mains huffering			
Mains builtage			
failure stored	5 ms; Refers to the power supply on the CPU section		
energy time			
<ul> <li>Repeat rate min</li> </ul>	1/s		
Input current			
Current			
consumption (rated	0.8 A: Digital opboard I/O modules are supplied separatoly		
value)	o.o A, Digital onboard 1/O modules are supplied separately		

Inrush current	
may	1.9 A; Rated value
1110.	0.04.42
	U.54 A <sup>2-</sup> S
Digital inputs	~
• from load voltage	
L+ (without load),	20 mA; per group
max.	
Digital outputs	1
• from load voltage	
	30 mA; Per group, without load
L+, max.	
output voltage / hea	der
Rated value (DC)	24 V
Encoder supply	· · · · · · · · · · · · · · · · · · ·
Number of outputs	2: One common 24 V encoder supply per 16 digital inputs
24 v encoder suppl	y
• 24 V	Yes; L+ (-0.8 V)
<ul> <li>Short-circuit</li> </ul>	Vaa
protection	res
• Output current,	1 A
	<u> </u>
Power	
Infeed power to the	10 W
backplane bus	
Power consumption	
from the backplane	9 W
hus (halanced)	- · · ·
Dower lace	
rower loss	
Power loss, typ.	15.2 W
Memory	
Number of slots for	
SIMATIC memory	1
SIMATIC memory	Yes
card required	
Work memory	·
integrated (for	
program)	250 kbyte
<ul> <li>Integrated (for</li> </ul>	1 Mbyte
data)	-,
Load memory	
Plug-in (SIMATIC	
Memory Card).	32 Ghyte
may	
васкир	
<ul> <li>maintenance-free</li> </ul>	Yes
CPU processing tim	es
for hit operations	
tun	48 ns
iyp.	
for word	58 ns
operations, typ.	
for fixed point	77 ns
arithmetic, tvp.	1110
for floating point	
arithmetic two	307 ns
Number of	4 000: Blocks (OB_EB_EC_DB) and LIDTs
elements (total)	
DB	
	1 60 999: subdivided into: number range that can be used by the
Number renge	user: 1 50,000, and number range of DPs created via SEC 96: 60
<ul> <li>Size, max.</li> </ul>	1 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
FB	
Number range	0 65 535
e Sizo mov	050 kbyto
• Size, max.	
FC	
<ul> <li>Number range</li> </ul>	0 65 535
• Size, max	250 kbyte
OB	
<ul> <li>Size, max.</li> </ul>	250 kbyte
<ul> <li>Number of free</li> </ul>	100
cycle OBs	100
Number of time	
	20
	<u> </u>

Number of delay	20
alarm OBs	
Number of cyclic	20; With minimum OB 3x cycle of 500 μs
Interrupt OBs	
Number of	50
process alarm OBs	
• Number of DPV1	3
alarm OBs	
<ul> <li>Number of</li> </ul>	
isochronous mode	1
OBs	
<ul> <li>Number of</li> </ul>	
technology	0
synchronous alarm	4
OBs	
<ul> <li>Number of</li> </ul>	100
startup OBs	100
Number of	
asynchronous error	4
OBs	
Number of	
	2
	4
Number of	l
	4
ulagnostic alarm	
URS	
Nesting depth	1
<ul> <li>per priority class</li> </ul>	24
Counters, timers an	d their retentivity
S7 counter	
Number	2 048
Betentivity	2010
	Maa
- adjustable	res
IEC counter	
Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
S7 times	1
Number	2 048
Detentivity	
REPUNIVITV	
	Voo
- adjustable	Yes
adjustable	Yes
adjustable IEC timer • Number	Yes Any (only limited by the main memory)
adjustable     IEC timer     Number     Retentivity	Yes Any (only limited by the main memory)
adjustable     IEC timer     Number     Retentivity     adjustable	Yes Any (only limited by the main memory) Yes
adjustable     IEC timer     Number     Retentivity     adjustable Data areas and thei	Yes Any (only limited by the main memory) Yes r retentivity
adjustable     IEC timer     Number     Retentivity     adjustable Data areas and thei     Retentive data area	Yes Any (only limited by the main memory) Yes ir retentivity
adjustable     IEC timer     Number     Retentivity     adjustable Data areas and thei Retentive data area (incl. timers	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte: In total: available retentive memory for bit memories, timers
adjustable     IEC timer     Number     Retentivity     adjustable Data areas and thei Retentive data area (incl. timers, counters flags)	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters_DBs_and technology data (axes): 88 KB
adjustable     IEC timer     Number     Retentivity     adjustable     Data areas and thei     Retentive data area     (incl. timers,     counters, flags),     max	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB
adjustable     IEC timer     Number     Retentivity     adjustable     Data areas and thei     Retentive data area     (incl. timers,     counters, flags),     max.     Extended rotentive	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB
adjustable     IEC timer     Number     Retentivity     adjustable     Data areas and thei     Retentive data area     (incl. timers,     counters, flags),     max.     Extended retentive	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB
adjustable     IEC timer     Number     Retentivity     adjustable     Data areas and thei     Retentive data area     (incl. timers,     counters, flags),     max.     Extended retentive     data area (incl.     timers     counters	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB 1 Mbyte; When using PS 6 0W 24/48/60 V DC HF
<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> </ul>	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB 1 Mbyte; When using PS 6 0W 24/48/60 V DC HF
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and their</li> <li>Retentive data areas</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive</li> <li>data area (incl. timers, counters, flags), max.</li> <li>Flag</li> </ul>	Yes Any (only limited by the main memory) Yes r retentivity 128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB 1 Mbyte; When using PS 6 0W 24/48/60 V DC HF
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and their</li> <li>Retentive data areas</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive</li> <li>data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8: 8 clock memory bit, grouped into one clock memory byte
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity adjustable</li> <li>Retentivity reset</li> <li>Local data</li> <li>per priority class, max.</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules
<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I/O address area</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I/O address area</li> <li>Inputs</li> <li>Outputs</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules         32 kbyte; All inputs are in the process image         32 kbyte; All outputs are in the process image
<ul> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I/O address area</li> <li>Inputs</li> <li>Outputs</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules         32 kbyte; All inputs are in the process image         32 kbyte; All outputs are in the process image
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<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I/O address area</li> <li>Inputs</li> <li>Outputs</li> <li>per integrated IO su</li> <li>Inputs (volume)</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules         32 kbyte; All inputs are in the process image         32 kbyte; All outputs are in the process image         8 kbyte
<ul> <li>adjustable</li> <li>adjustable</li> <li>IEC timer</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>Data areas and thei</li> <li>Retentive data area</li> <li>(incl. timers, counters, flags), max.</li> <li>Extended retentive data area (incl. timers, counters, flags), max.</li> <li>Flag</li> <li>Size, max.</li> <li>Number of clock memories</li> <li>Data blocks</li> <li>Retentivity adjustable</li> <li>Retentivity adjustable</li> <li>Retentivity adjustable</li> <li>Retentivity class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I/O address area</li> <li>Inputs</li> <li>Outputs</li> <li>per integrated IO su</li> <li>Inputs (volume)</li> </ul>	Yes         Any (only limited by the main memory)         Yes         r retentivity         128 kbyte; In total; available retentive memory for bit memories, timers counters, DBs, and technology data (axes): 88 KB         1 Mbyte; When using PS 6 0W 24/48/60 V DC HF         16 kbyte         8; 8 clock memory bit, grouped into one clock memory byte         Yes         No         64 kbyte; max. 16 KB per block         2 048; max. number of modules / submodules         32 kbyte; All inputs are in the process image         32 kbyte; All outputs are in the process image         8 kbyte

- Outputs	8 kbyte
(volume)	
per CM/CP	
Inputs (volume)	8 kbyte
- Outputs	8 kbyte
(volume)	
Subprocess images	
<ul> <li>Number of</li> </ul>	
subprocess	32
images, max.	
Hardware configura	tion
Number of	32; A distributed I/O system is characterized not only by the integration
distributed IO	of distributed I/O via PROFINE I or PROFIBUS communication
systems	modules, but also by the connection of I/O via AS-I master modules or
	links (e.g. IE/PB-Link)
Number of DP mast	
● Via CM	6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINE I, Ethernet) can be inserted in total
Number of IO Contr	rollers
<ul> <li>integrated</li> </ul>	1
• Via CM	6; A maximum of 6 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total
Rack	l
<ul> <li>Modules per</li> </ul>	32° CPLL + 31 modules
rack, max.	
Number of lines.	1
max.	
PtP CM	1
Number of PtP	the number of connectable PtP CMs is only limited by the number of
CMs	available slots
Time of day	
Clock	
• Туре	Hardware clock
<ul> <li>Backup time</li> </ul>	6 wk; At 40 °C ambient temperature, typically
Deviation per	10 c: Typ : 2 c
day, max.	10 S, 19µ 2 S
Operating hours con	unter
Number	16
Clock synchronizati	on
<ul> <li>supported</li> </ul>	Yes
• in AS, master	Yes
• in AS, slave	Yes
on Ethernet via	
NTP Digital inputs	Yes
(DI)	32
Digital inputs, parameterizable	Yes
Source/sink input	P-reading
Input characteristic	
curve in	Vac
accordance with	res
IEC 61131, type 3	
Digital input function	ns, parameterizable
Gate start/stop	Yes
Capture	Yes
<ul> <li>Synchronization</li> </ul>	Yes
	1

iensions		
Width	110 mm	_
Height	147 mm	
Depth	129 mm	
Weights		
Weight, approx.	1 360 g	





**VOBOAL** Shenzhen Voboal Industrial Automation Co., Ltd.

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