SIEMENS PLC SIMATIC S7-1500CPU 1513F-1 PN 6ES7513-1FL02-0AB0 MEMORY CARD NECESSARY

Basic Information

Place of Origin: Germany
Brand Name: SIEMENS
Certification: CE

Model Number: PLC SIMATIC S7-1500CPU 1513F-1 PN

6ES7513-1FL02-0AB0

Minimum Order Quantity: 1Price: USD

• Packaging Details: 15,10 x 15,40 x 4,60

Delivery Time: 10-12Days
Payment Terms: L/C, T/T
Supply Ability: 100



Product Specification

 Number Of Communication 8 CM Modules:

Cpu Type: S7-1500
 Number Of Digital Outputs: 32 DO
 Number Of Analog Inputs: 8 AI
 Programming Software: TIA Portal
 Number Of Analog Outputs: 8 AO

• Integrated Functions: Web Server, OPC UA Server, Security

 Number Of Technology 32 TM Modules:

• Number Of Motion Control 32 MC

Axes:

Number Of Digital Inputs: 32 DI
 Power Supply: 24 V DC
 Operating System: Windows 7/10

Product Description

SIEMENS PLC SIMATIC S7-1500CPU 1513F-1 PN 6ES7513-1FL02-0AB0 MEMORY CARD NECESSARY Product Introduction:

The SIEMENS PLC SIMATIC S7-1500 CPU 1513-1 FL 6ES7513-1FL02-0AB0 is a central processing unit (CPU) specifically designed for industrial automation applications. It belongs to the SIEMENS SIMATIC S7-1500 series, renowned for its advanced functionality, high performance, and reliability.

Product Information and Specifications:

- Model: CPU 1513-1 FL 6ES7513-1FL02-0AB0

The CPU 1513-1 FL is equipped with a powerful processor that ensures fast and efficient execution of control programs. It supports multiple programming languages, including ladder logic, function blocks, and structured text, providing flexibility and ease of use for complex control tasks.

In terms of memory capacity, the CPU 1513-1 FL offers ample storage space for both program and data. While specific details were not provided in the query, typical configurations of the CPU include program memory ranging from 300 KB to 900 KB and data memory ranging from 200 KB to 500 KB. This memory capacity allows users to store their control programs and necessary data structures for the PLC's operation.

Designed to operate in demanding industrial environments, the CPU 1513-1 FL delivers reliable and precise control for applications such as manufacturing, process control, and machine automation. It supports a wide range of communication interfaces, facilitating seamless integration with other devices and systems within the automation network.

The CPU 1513-1 FL is typically programmed and configured using Siemens' TIA Portal (Totally Integrated Automation Portal) software. The TIA Portal provides a comprehensive engineering environment for efficient programming, simulation, and diagnostics, ensuring easy development and maintenance of automation projects.

- Model: CPU 1513-1 FL 6ES7513-1FL02-0AB0
- Processor: Powerful processor for fast and efficient control program execution
- Programming Languages: Supports ladder logic, function blocks, and structured text
- Memory Capacity: Sufficient storage space for program and data
- Communication Interfaces: Supports various communication interfaces
- Engineering Software: Programmed and configured using Siemens' TIA Portal
- Suitable for: Manufacturing, process control, and machine automation applications

In summary, the SIEMENS PLC SIMATIC S7-1500 CPU 1513-1 FL 6ES7513-1FL02-0AB0 is a dependable CPU featuring advanced functionality, ample memory capacity, and seamless communication capabilities. It offers efficient and precise control for various industrial processes, making it well-suited for a wide range of industrial automation applications.

| General information | | | | |
|---|---|--|--|--|
| Product type | CPU 1513F-1 PN | | | |
| designation | | | | |
| HW functional status | FS03 | | | |
| Firmware version | V2.9 | | | |
| Product function | | | | |
| I&M data | Yes; I&M0 to I&M3 | | | |
| Isochronous mode | Yes; Distributed and central; with minimum OB 6x cycle of 500 μs (distributed) and 1 ms (central) | | | |
| Engineering with | | | | |
| ■ STEP 7 TIA Portal | V17 (FW V2.9) / V15 (FW V2.5) or higher; with older TIA Portal | | | |
| configurable/integrated | versions configurable as 6ES7513-1FL01-0AB0 | | | |
| from version | Versions sormgulable as seen site in each sites | | | |
| Configuration control | | | | |
| via dataset | Yes | | | |
| Display | | | | |
| Screen diagonal [cm] | 3.45 cm | | | |
| Control elements | | | | |
| Number of keys | 8 | | | |
| Mode buttons | 2 | | | |
| Supply voltage | | | | |
| Rated value (DC) | 24 V | | | |
| permissible range, | 40.0 \ | | | |
| lower limit (DC) | 19.2 V | | | |
| permissible range, | 28.8 V | | | |
| upper limit (DC) | 20.0 V | | | |
| Reverse polarity | Yes | | | |
| protection | 165 | | | |
| Mains buffering | | | | |
| Mains/voltage failure | 5 ms | | | |
| stored energy time | | | | |
| Repeat rate, min. | [1/s | | | |
| Input current | | | | |
| Current consumption | 0.7 A | | | |
| (rated value) | 5 / \ | | | |
| Current consumption, | 0.95 A | | | |
| max. | | | | |
| Inrush current, max. | 1.9 A; Rated value | | | |
| I ² t | 0.02 A ² ·s | | | |
| Power | | | | |
| Infeed power to the | 10 W | | | |
| backplane bus | | | | |
| | | | | |

| | * |
|--|---|
| Power consumption | |
| from the backplane bus | 5.5 W |
| (balanced) | |
| Power loss | |
| Power loss, typ. | 5.7 W |
| Memory | |
| Number of slots for | 1 |
| SIMATIC memory card | |
| SIMATIC memory card | Yes |
| required | 163 |
| Work memory | |
| integrated (for | 450 kbyte |
| program) | 450 kbyte |
| integrated (for data) | 1.5 Mbyte |
| Load memory | |
| ● Plug-in (SIMATIC | 22 Chyta |
| Memory Card), max. | 32 Gbyte |
| Backup | |
| maintenance-free | Yes |
| CPU processing times | |
| for bit operations, typ. | 40 ns |
| for word operations, | |
| typ. | 48 ns |
| for fixed point | |
| arithmetic, typ. | 64 ns |
| for floating point | |
| arithmetic, typ. | 256 ns |
| CPU-blocks | I |
| Number of elements | I |
| (total) | 4 000; Blocks (OB, FB, FC, DB) and UDTs |
| DB | |
| | 1 60 999; subdivided into: number range that can be used by the |
| Number range | user: 1 59 999, and number range of DBs created via SFC 86: 60 000 60 999 |
| • Size, max. | 1.5 Mbyte; For DBs with absolute addressing, the max. size is 64 KB |
| FB | |
| Number range | 0 65 535 |
| Size, max. | 450 kbyte |
| FC | |
| Number range | 0 65 535 |
| Size, max. | 450 kbyte |
| OB | <u>'</u> |
| ● Size, max. | 150 khyto |
| | 450 kbyte |
| Number of free cycle | liaa |
| OBs | 100 |
| OBs Number of time alarm OBs | |
| OBs Number of time alarm OBs Number of delay | |
| OBs Number of time alarm OBs Number of delay alarm OBs | 20 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic | 20 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs | 20 20 20; With minimum OB 3x cycle of 500 μs |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process | 20 20 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs | 20 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 | 20 20 20; With minimum OB 3x cycle of 500 μs |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs | 20 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup | 20 20; With minimum OB 3x cycle of 500 μs 50 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of startup OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of startup OBs Number of asynchronous error | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of synchronous error OBs Number of diagnostic | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of asynchronous error OBs Number of asynchronous error OBs Number of synchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of asynchronous error OBs Number of asynchronous error OBs Number of synchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 2 1 |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of asynchronous error OBs Number of asynchronous error OBs Number of synchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 22 1 100 24; Up to 8 possible for F-blocks |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of startup OBs Number of asynchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs Number of diagnostic alarm OBs | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 22 1 100 24; Up to 8 possible for F-blocks |
| OBs Number of time alarm OBs Number of delay alarm OBs Number of cyclic interrupt OBs Number of process alarm OBs Number of DPV1 alarm OBs Number of isochronous mode OBs Number of technology synchronous alarm OBs Number of asynchronous error OBs Number of asynchronous error OBs Number of synchronous error OBs Number of synchronous error OBs Number of diagnostic alarm OBs Nesting depth per priority class | 20 20; With minimum OB 3x cycle of 500 μs 50 3 2 100 4 22 1 100 24; Up to 8 possible for F-blocks |

| Number | 2 048 |
|---|--|
| Retentivity | |
| — adjustable | Yes |
| IEC counter | • |
| Number | Any (only limited by the main memory) |
| Retentivity | y my (emy miniou by the main memory) |
| — adjustable | Yes |
| S7 times | 1165 |
| | 0.040 |
| Number | 2 048 |
| Retentivity | |
| — adjustable | Yes |
| IEC timer | |
| Number | Any (only limited by the main memory) |
| Retentivity | |
| — adjustable | Yes |
| Data areas and their re | |
| Retentive data area | T |
| (incl. timers, counters, | 128 kbyte; In total; available retentive memory for bit memories, |
| Γ ' | timers, counters, DBs, and technology data (axes): 88 KB |
| flags), max. | |
| Extended retentive | 4 F Mb to Mb on voice DO C OM O4/40/CO V DO HE |
| data area (incl. timers, | 1.5 Mbyte; When using PS 6 0W 24/48/60 V DC HF |
| counters, flags), max. | |
| Flag | |
| Size, max. | 16 kbyte |
| Number of clock | 8; 8 clock memory bit, grouped into one clock memory byte |
| memories | o, a clock memory bit, grouped into one clock memory byte |
| Data blocks | • |
| Retentivity adjustable | Yes |
| Retentivity preset | No |
| Local data | I TO |
| | |
| per priority class, | 64 kbyte; max. 16 KB per block |
| max. | of Royle, max. To RD per blook |
| Address area | |
| | 2 048; max. number of modules / submodules |
| I/O address area | 2 040, max. number of modules / submodules |
| | |
| Inputs | 32 kbyte; All inputs are in the process image |
| Outputs | 32 kbyte; All outputs are in the process image |
| per integrated IO subsy | rstem |
| Inputs (volume) | 8 kbyte |
| — Outputs (volume) | 8 kbyte |
| per CM/CP | 1 , |
| Inputs (volume) | 8 kbyte |
| — Outputs (volume) | • |
| | 8 kbyte |
| Subprocess images | |
| Number of | |
| subprocess images, | 32 |
| max. | |
| Hardware configuration | |
| | 32; A distributed I/O system is characterized not only by the |
| Number of distributed | integration of distributed I/O via PROFINET or PROFIBUS |
| IO systems | communication modules, but also by the connection of I/O via AS-i |
| | master modules or links (e.g. IE/PB-Link) |
| Number of DP masters | |
| | 6; A maximum of 6 CMs (PROFINET + PROFIBUS) can be |
| Via CM | inserted in total |
| Number of IO Controlle | 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - |
| | 10 |
| integrated | O. A service of COM (PROFILET BROSES) |
| • Via CM | 6; A maximum of 6 CMs (PROFINET + PROFIBUS) can be |
| | inserted in total |
| Rack | |
| Modules per rack, | 32; CPU + 31 modules |
| max. | OL, OI O T OI MOUNIO |
| Number of lines, | |
| | 1 |
| max. | 1 |
| max. PtP CM | 1 |
| PtP CM | |
| | the number of connectable PtP CMs is only limited by the number |
| PtP CM Number of PtP CMs | |
| PtP CM • Number of PtP CMs Time of day | the number of connectable PtP CMs is only limited by the number |
| PtP CM • Number of PtP CMs Time of day Clock | the number of connectable PtP CMs is only limited by the number of available slots |
| PtP CM • Number of PtP CMs Time of day Clock • Type | the number of connectable PtP CMs is only limited by the number of available slots Hardware clock |
| PtP CM Number of PtP CMs Time of day Clock Type Backup time | the number of connectable PtP CMs is only limited by the number of available slots |
| PtP CM • Number of PtP CMs Time of day Clock • Type | the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically |
| PtP CM Number of PtP CMs Time of day Clock Type Backup time | the number of connectable PtP CMs is only limited by the number of available slots Hardware clock |
| PtP CM Number of PtP CMs Time of day Clock Type Backup time Deviation per day, | the number of connectable PtP CMs is only limited by the number of available slots Hardware clock 6 wk; At 40 °C ambient temperature, typically 10 s; Typ.: 2 s |

| Number | 16 |
|--|--|
| Clock synchronization | |
| • supported | Yes |
| | 1 |
| in AS, master | Yes |
| in AS, slave | Yes |
| on Ethernet via NTP | Yes |
| | 100 |
| Interfaces | |
| Number of PROFINET | 1 |
| interfaces | |
| | |
| 1. Interface | |
| Interface types | |
| RJ 45 (Ethernet) | Yes; X1 |
| , , | |
| Number of ports | 2 |
| integrated switch | Yes |
| Protocols | |
| IP protocol | Yes; IPv4 |
| | 165, 1744 |
| PROFINET IO | Yes |
| Controller | |
| PROFINET IO | |
| Device | Yes |
| | |
| ● SIMATIC | Voc |
| communication | Yes |
| Open IE | |
| - P - | Yes; Optionally also encrypted |
| communication | , |
| Web server | Yes |
| Media redundancy | Yes; MRP Automanager according to IEC 62439-2 Edition 2.0 |
| | |
| PROFINET IO Controlle | er |
| Services | |
| – PG/OP | |
| 1 | Yes |
| communication | |
| — Isochronous mode | Yes |
| — Direct data | Very Demoisson to IDT and the last of the Company o |
| exchange | Yes; Requirement: IRT and isochronous mode (MRPD optional) |
| | |
| ⊢ IRT | Yes |
| — PROFlenergy | Yes; per user program |
| — Prioritized startup | Yes; Max. 32 PROFINET devices |
| | 1 00, WIGA. OF LIGHT HAFT REVICES |
| — Number of | 128; In total, up to 512 distributed I/O devices can be connected |
| connectable IO | via AS-i, PROFIBUS or PROFINET |
| Devices, max. | για Λυτί, ΓΠΟΙ ΙΒΟΌ ΟΙ ΓΠΟΓΙΝΕΊ |
| — Of which IO devices | |
| | 64 |
| with IRT, max. | |
| — Number of | |
| connectable IO Devices | s 128 |
| for RT, max. | |
| | |
| — of which in line, | 128 |
| max. | |
| — Number of IO | |
| Devices that can be | |
| simultaneously | 8; in total across all interfaces |
| , | o, in total across all interfaces |
| activated/deactivated, | |
| max. | |
| — Number of IO | |
| Devices per tool, max. | 8 |
| Edvices per tool, max. | The minimum value of the wadate time also decreased as |
| 1 | The minimum value of the update time also depends on |
| Updating times | communication share set for PROFINET IO, on the number of IO |
| 1 | devices, and on the quantity of configured user data |
| Update time for IRT | , |
| Speaks time for fitt | DEO us to 4 may Note in the case of IDT will be |
| for send cycle of 250 | 250 μs to 4 ms; Note: In the case of IRT with isochronous mode, |
| 1 | the minimum update time of 500 µs of the isochronous OB is |
| μs | decisive |
| for send cycle of 500 | j |
| — for send cycle of 500 | /j500 μs to 8 ms |
| μο | |
| — for send cycle of 1 | 1 ms to 16 ms |
| ms | מוו טו טווס וטווסו טוווס |
| 1 - | |
| for cond avala of 0 | 0 |
| for send cycle of 2 | 2 ms to 32 ms |
| ms | 2 ms to 32 ms |
| ms | |
| ms — for send cycle of 4 | 4 ms to 64 ms |
| ms — for send cycle of 4 ms | |
| ms — for send cycle of 4 ms — With IRT and | 4 ms to 64 ms |
| ms — for send cycle of 4 ms — With IRT and parameterization of | 4 ms to 64 ms Update time = set "odd" send clock (any multiple of 125 μs: 375 |
| ms — for send cycle of 4 ms — With IRT and | 4 ms to 64 ms |
| ms — for send cycle of 4 ms — With IRT and parameterization of | 4 ms to 64 ms Update time = set "odd" send clock (any multiple of 125 μs: 375 |

| Dimensions | |
|------------|--------|
| Width | 35 mm |
| Height | 147 mm |

| Depth | 129 mm |
|-----------------|--------|
| Weights | |
| Weight, approx. | 405 g |



VOBOAL Shenzhen Voboal Industrial Automation Co., Ltd.



+8613760462017



sale@voboal.com



plcsimatic.com

Sienteng Zhongbao Industrial Park, Longdong Community, Baolong Street, Longgang District, Shenzhen