## SIEMENS PLC SIMATIC S7-1500 CPU 1518T-4 PN/DP 6ES7518-4TP00-0AB0 **WORK MEMORY FOR PROGRAM**

## **Basic Information**

. Place of Origin: Germany . Brand Name: SIEMENS · Certification: CE

Model Number: 6ES7518-4TP00-0AB0

• Price: USD

15,10 x 15,40 x 4,60 Packaging Details:

. Delivery Time: 10-12Days • Payment Terms: L/C, T/T • Supply Ability: 100



## **Product Specification**

• Number Of Digital Outputs: 32

• Operating Temperature -20 To 60 Degrees Celsius

Range:

• Number Of Usb Interfaces: 2 Number Of Profibus Interfaces:

 Number Of Industrial Ethernet Interfaces:

Number Of Analog Outputs: 8

• Number Of Analog Inputs: 8 • Number Of Sd Card Slots: 1

• Number Of Communication 8 Modules:

 Number Of Profinet Interfaces:

 Number Of Technology 32

Modules:

## **Product Description**

SIEMENS PLC SIMATIC S7-1500 CPU 1518T-4 PN/DP 6ES7518-4TP00-0AB0 WORK MEMORY FOR PROGRAM Product Introduction:

The SIEMENS PLC SIMATIC S7-1500 CPU 1518-4 TP 6ES7518-4TP00-0AB0 is a powerful and reliable central processing unit (CPU) designed for industrial automation applications. It belongs to the SIEMENS SIMATIC S7-1500 series, known for its high performance, flexibility, and advanced features.

Product Information and Specifications:

- Model: CPU 1518-4 TP 6ES7518-4TP00-0AB0

The CPU 1518-4 TP is equipped with a high-performance processor that ensures fast and efficient execution of control programs. It supports multiple programming languages, including ladder logic, function blocks, and structured text, providing flexibility and ease of use for various control tasks.

In terms of memory capacity, the CPU 1518-4 TP offers ample storage space for programs and data. Although specific details were not provided in the query, typical configurations of the CPU include program memory ranging from 12 MB to 20 MB and data memory ranging from 12 MB to 20 MB. This allows users to store large and complex control programs and data structures required for the PLC's operation.

The CPU 1518-4 TP is designed to handle demanding automation applications and complex control tasks. It offers advanced features such as high-speed communication interfaces, built-in diagnostics, and security functions, enabling seamless integration with other devices and systems within the automation network.

Programming and configuration of the CPU 1518-4 TP are typically performed using Siemens' TIA Portal (Totally Integrated Automation Portal) software. The TIA Portal provides a comprehensive engineering environment for efficient programming, simulation, and diagnostics, ensuring streamlined development and maintenance of automation projects.

- Model: CPU 1518-4 TP 6ES7518-4TP00-0AB0
- Processor: High-performance processor for fast control program execution
- Programming Languages: Supports ladder logic, function blocks, and structured text
- Memory Capacity: Ample storage space for programs and data
- Advanced Features: High-speed communication interfaces, built-in diagnostics, security functions
- Engineering Software: Programmed and configured using Siemens' TIA Portal
- High Performance: Designed for demanding automation applications and complex control tasks

In summary, the SIEMENS PLC SIMATIC S7-1500 CPU 1518-4 TP 6ES7518-4TP00-0AB0 is a powerful and reliable CPU offering high performance, ample memory capacity, and advanced features. It is designed to handle demanding automation applications with precision and efficiency. With its advanced functionalities and flexibility, it is an ideal choice for a wide range of industrial automation applications, ensuring reliable and high-performance control.

General information		
Product type	CPU 1518T-4 PN/DP	
designation	01 0 13101-41 14/01	
HW functional	FS11	
status		
Firmware version	V3.1	
<ul><li>FW update</li></ul>	Yes	
possible	100	
Product function		
<ul><li>I&amp;M data</li></ul>	Yes; I&M0 to I&M3	
<ul><li>Isochronous</li></ul>	Yes; Distributed and central; with minimum OB 6x cycle of 125 μs	
mode	(distributed) and 1 ms (central)	
<ul><li>SysLog</li></ul>	Yes	
Engineering with		
● STEP 7 TIA		
Portal	V19 (FW V3.1) / V17 (FW V2.9) or higher	
configurable/integra	l	
ted from version		
Configuration contro		
via dataset	Yes	
Display		
Screen diagonal	6.1 cm	
[cm]	0.1 0111	
Control elements		
Number of keys	6	
Mode selector	1	
switch		
Supply voltage		
Rated value (DC)	24 V	
permissible range,	19.2 V	
lower limit (DC)	10.2 V	
permissible range,	28.8 V	
upper limit (DC)		
Reverse polarity	Yes	
protection		
Mains buffering		
<ul><li>Mains/voltage</li></ul>		
failure stored	5 ms	
energy time		
-	1/s	
Input current		

Current	
consumption (rated	1.55 A
value)	
Current	
consumption, max.	1.9 A
Inrush current,	
	1.9 A; Rated value
max.	
l <sup>2</sup> t	0.4 A²⋅s
Power	
Infeed power to the	Ī
backplane bus	12 W
Power consumption	
from the backplane	50 VV
bus (balanced)	
Power loss	
Power loss, typ.	24 W
Memory	
Number of slots for	
I	1
SIMATIC memory	1
card	
SIMATIC memory	Yes
card required	
Work memory	
• integrated (for	0.10
program)	9 Mbyte
integrated (for data)	60 Mbyte
data)	
Load memory	
<ul> <li>Plug-in (SIMATIC</li> </ul>	
Memory Card),	32 Gbyte
max.	
Backup	
	V
maintenance-free	
CPU processing tim	es
for bit operations,	1 ns
typ.	115
for word	
operations, typ.	2 ns
for fixed point	
	2 ns
arithmetic, typ.	
for floating point	6 ns
arithmetic, typ.	
CPU-blocks	
Number of	
elements (total)	20 000; Blocks (OB, FB, FC, DB) and UDTs
DB	
	1 60 999; subdivided into: number range that can be used by the
<ul><li>Number range</li></ul>	user: 1 59 999, and number range of DBs created via SFC 86: 60
	000 60 999
<ul><li>Size, max.</li></ul>	16 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
FB	1 , , , , , , , , , , , , , , , , , , ,
	0 65 535
Number range	0 65 535
<ul><li>Size, max.</li></ul>	1 Mbyte
FC	
Number range	0 65 535
Size, max.	1 Mbyte
OB	j
	1 Mbuta
Size, max.	1 Mbyte
<ul> <li>Number of free</li> </ul>	100
cycle OBs	1.55
<ul> <li>Number of time</li> </ul>	20
alarm OBs	20
Number of delay	
alarm OBs	20
I	
Number of cyclic	20; with minimum OB 3x cycle of 100 μs
interrupt OBs	<u> </u>
<ul><li>Number of</li></ul>	50
process alarm OBs	P <sup>O</sup>
<ul> <li>Number of DPV1</li> </ul>	
alarm OBs	3
Number of	
L 1 11 1 1	
I .	3
OBs	

lecknology synchronous alarm OBs  • Number of startup OBs  • Number of startup OBs  • Number of synchronous error OBs  • Number of OBs  • Number of or Synchronous error OBs  • Number of oldingmostic alarm OBs  • Number of oldingmostic alarm OBs  • Number of oldingmostic alarm OBs  • Perpriority class  • Counters  • Number  Retentivity  — adjustable  • Number  • Number  Retentivity  — adjustable  • Ves  • IEC immer  • Number  • Number  Retentivity  — adjustable  • Number of look  memories  • Data areas and their retentivity  Retentive data area  [Incl. times, counters, flags), max.  Flag  • Size, max.  • Number of look  memories  • Number of look  memories  • Retentivity  • All is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, max.  Is byte; When using PS 6 0W 24/48/60 V DC HF  incl. times, counters, flags, m	. Niversia av af	
Synchronous alarm OBs  ■ Number of startup OBs  ■ Number of asynchronous error obs  ■ Number of asynchronous error obs  ■ Number of diagnostic alarm OBs  Nosting depth  ■ Per priority class 24  Counters, timers and their retentivity  ■ adjustable  ■ Number Padjustable  ■ Number Padjus	Number of	
OBS ■ Number of startup OBS ■ Number of startup OBS ■ Number of synchronous error OBS ■ Number of synchronous error OBS ■ Number of synchronous error OBS ■ Number of diagnostic alarm OBS ■ Number of observation of the properties of the		2
■ Number of startup OBs ■ Number of synchronous error OBs ■ Number of synchronous error OBs ■ Number of diagnostic alarm OBs Nesting depth ■ per priority class   24	l '	
Startup OBs   Number of asynchronous error		
Number of asynchronous error dobs Number of diagnostic alarm of the per priority class 24 Counters, timers and their retentivity S7 counter Number   2 048 Retentivity		100
asynchronous error OBs  • Number of synchronous error OBs  • Number of Idagnostic alarm OBs  • Number of Idagnostic alarm OBs  • Per priority class   24		
OBs Number of synchronous error OBs Number of synchronous error OBs Nesting depth Depriying class   24 Counters, timers and their retentivity S7 counter Number   2 048 Retentivity   2 048 Retentivity   4		
■ Number of systems and their retentivity retentivit	'	4
Synchronous error OBs  Number of diagnostic alarm OBs Nosting depth   Det priority class		
OBs Number of diagnostic alarm OBs Number priority class 24 Counters, timers and their retentivity S7 counter Number   2048 Retentivity   adjustable   Yes   Ecumber   Any (only limited by the main memory) Retentivity   adjustable   Yes   Ecumber   Any (only limited by the main memory) Retentivity   adjustable   Yes   ECUmber   Any (only limited by the main memory) Retentivity   adjustable   Yes   ECUMBER   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   And   Any (only limited by the main memory) Retentivity   Any (only limited by the main memory) Rete	I '. '' '	
Number of diagnostic alarm of Description of Descr	-,	[2
diagnostic alarm   1   0   0   0   0   0   0   0   0   0		
OBS Nesting depth  Per priority class   24 Counters, timers and their retentivity   75 counter   2 048 Retentivity		
Nesting depth  Per priority class Counters, timers and their retentivity Number	0	1
● per priority class   24 Counters, timers and their retentivity S7 counter  ● Number   2 048 Retentivity   — adjustable   Yes   IEC counter   White   Yes   IEC counter   White   Yes   Retentivity   — adjustable   Yes   S7 times   White   Yes   S8 times   White   Yes   S9 times   White   White   White   White   White   S9 times   White   White   White   White   White   S9 times   White   White   White   White   White   S9 times   White   White   White   White   White   White   S9 times   White   White   White   White   White   White   S9 times   White		
Counters, timers and their retentivity  7 counter  Number  Retentivity  — adjustable  EC counter  Number  Any (only limited by the main memory)  Retentivity — adjustable  Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  16 kbyte; When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS 6 0W 24/48/60 V DC HF  When using PS		
S7 counter  Number Number Number Retentivity — adjustable	_ ' ' '	
Retentivity - adjustable   Yes		d their retentivity
Retentivity — adjustable   EC counter   Number   Any (only limited by the main memory)   Retentivity   Adjustable   Yes	S7 counter	
adjustable   Yes   IEC counter   Number   Any (only limited by the main memory) Retentivity   adjustable   Yes   St limes   Number   2 048   Retentivity   adjustable   Yes   IEC timer   Any (only limited by the main memory) Retentivity   adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   Any (only limited by the main memory for bit memories, timers, counters, adjustable   Any (only limited by the main memory) Retentivity   Any (only limited by the main memory) Retentivity   Any (only limited by the main memory   Any (only limite	<ul><li>Number</li></ul>	2 048
IEC counter  Number Any (only limited by the main memory) Retentivity — adjustable   Yes   S7 times   Number   2 048   Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   Data areas and their retentivity   Retentive data area   (incl. timers, counters, flags), max.   Counters, flags), max.   Sextended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB   Size, max.	Retentivity	
IEC counter  Number Any (only limited by the main memory) Retentivity — adjustable   Yes   S7 times   Number   2 048   Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   IEC timer   Number   Any (only limited by the main memory) Retentivity   — adjustable   Yes   Data areas and their retentivity   Retentive data area   (incl. timers, counters, flags), max.   Counters, flags), max.   Sextended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB   Size, max.		Yes
Retentivity — adjustable Yes S7 times  ● Number   2 048   Retentivity — adjustable   Yes   Retentive data area a (incl. timers, counters, flags), max.   Fishing   Size, max.   16 kbyte; In total; available retentive memory for bit memories, timers, counters, flags), max.   Fishing   Yes		1
Retentivity — adjustable Yes  Number   2 048   Retentivity — adjustable   Yes   EC timer   Any (only limited by the main memory) Retentivity — adjustable   Yes   EC timer   Any (only limited by the main memory) Retentivity — adjustable   Yes   Data areas and their retentivity   Retentive data area (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, flags), max.   Elag   Size, max.		Any (only limited by the main memory)
adjustable Yes \$7 times Number   2 048 Retentivity		y ary torny infinited by the main memory)
S7 times  ● Number  ■ Retentivity  — adjustable   Yes   IEC timer  ● Number   Any (only limited by the main memory)  Retentivity — adjustable   Yes   Data areas and their retentivity Retentive data area   (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, Bags), max.   Extended retentive data area (incl. timers, counters, liags), max.   Extended retentive data area (incl. timers, counters, liags), max.   Extended retentive data area (incl. timers, counters, liags), max.   Flag  ● Size, max.   16 kbyte  ● Number of clock memories   3; 8 clock memory bit, grouped into one clock memory byte   Data blocks   4 kbyte; max. 16 KB per block   Address area   Inputs   32 kbyte; max. 16 KB per block   Address area   Inputs   32 kbyte; All inputs are in the process image   Der Integrated IO subsystem   Inputs (volume)   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dept CMCP   Inputs (volume)   8 kbyte   Dutputs (volume)   9 kbyte   Dutputs (volume		Voo
Number		T ES
Retentivity — adjustable   Yes   IEC timer  Number   Any (only limited by the main memory) Retentivity — adjustable   Yes   Data areas and their Retentivity   Retentive data area (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, flags), max.   Extended retentive data area (incl. timers, counters, flags), max.   Flag   0		
adjustable   Yes		2 048
IEC timer	Retentivity	
Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  20 Mbyte; When using PS 6 0W 24/48/60 V DC HF    Size, max	— adjustable	Yes
Retentivity — adjustable Yes  Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Flag Size, max.  Number of clock memories Data blocks Retentivity preset No Local data  per priority class, max.  If 6 kbyte; max. 16 KB per block Address area Number of IO modules I/O address area Number of IO modules I/O address area I/O subystem I/O suby	IEC timer	·
Retentivity — adjustable Yes  Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Flag Size, max.  Number of clock memories Data blocks Retentivity preset No Local data  per priority class, max.  If 6 kbyte; max. 16 KB per block Address area Number of IO modules I/O address area Number of IO modules I/O address area I/O subystem I/O suby	Number	Any (only limited by the main memory)
Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Flag Size, max.  Number of clock memories Retentivity adjustable Retentivity adjustable Retentivity adjustable Retentivity and tasta area (incl. timers, counters, flags), max.  Flag Size, max.  16 kbyte Number of clock memories Retentivity adjustable Retentivity and tasta area (incl. timers, counters, flags), max.  Flag Size, max.  16 kbyte Number of clock memory bit, grouped into one clock memory byte Data blocks Retentivity adjustable Retentivity preset No Local data Per priority class, max.  Address area Number of IO modules Ioadress area Inputs Volumes Size, max. 16 kB per block Address area Inputs Vas Size, max. 16 kB per block Address area Inputs Vas Size, max. 16 kB per block Address area Size, max. 16 kB per	Retentivity	, , , , , , , , , , , , , , , , , , ,
Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB max.  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB max.  Extended retentive data area (incl. timers, counters, liags), max.  Flag  Size, max.  Number of clock memories  Data blocks  Retentivity preset No  Local data  Per priority class, max.  64 kbyte; max. 16 KB per block  Address area  Number of IO modules  Noutputs  Outputs  Outputs  Outputs  Size kbyte; All inputs are in the process image  Per integrated IO subsystem  Inputs (volume)  Inputs (volume)  Size kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Outputs  (volume)  Retentivity preset No  Base (incl. timers, counters, DBs, and technology data (axes): 700 KB  Mbyte; In total; available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 700 KB  Retentivity preset No  Base (lock memory byte Description one clock		Vos
Retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data area (incl. timers, counters, DBs, and technology data (axes): 700 KB  Extended retentive data (axes): 700 KB  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When using PS 6 0W 24/48/60 V DC HF  ### When us	aujustable	165
(incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Flag Size, max.  Number of clock memory bit, grouped into one clock memory byte describing byte max.  Retentivity adjustable Retentivity preset No Local data Per priority class, max.  Inputs Address area Altinute discussive max.  Address area Inputs Address area Altinute discussive max.  Altinute memorives, timers, counters, DBs, and technology data (axes): 700 KB  Altinute discussive memory box delivers, DBs, and technology data (axes): 700 KB  Altinute discussive memory bit, grouped into one clock memory byte  At bytes  At byte	Data areas and thei	retentivity
counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Plag Size, max.  16 kbyte Number of clock memories  Data blocks Retentivity preset Retentivity preset Retentivity preset Retentivity preset Ray Size, max.  16 kbyte Size, max.  16 kB per block memory byte  16 384; max. 16 kB per block  16 384; max. number of modules / submodules  16 384; max. number of modules / submodules  17 address area  18 linputs Size, max.  19 linputs Size, kbyte; All inputs are in the process image Size per integrated IO subsystem  Inputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte Size outputs Size ou	Retentive data area	
counters, flags), max.  Extended retentive data area (incl. timers, counters, flags), max.  Plag Size, max.  16 kbyte Number of clock memories  Data blocks Retentivity preset Retentivity preset Retentivity preset Retentivity preset Ray Size, max.  16 kbyte Size, max.  16 kB per block memory byte  16 384; max. 16 kB per block  16 384; max. number of modules / submodules  16 384; max. number of modules / submodules  17 address area  18 linputs Size, max.  19 linputs Size, kbyte; All inputs are in the process image Size per integrated IO subsystem  Inputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte; max. 32 kB via X1; max. 8 kB via X2 or X4  Outputs (volume) Size kbyte Size outputs Size ou	(incl. timers,	768 kbyte; In total; available retentive memory for bit memories, timers,
max.  Extended retentive data area (incl. timers, counters, flags), max.  Flag  Size, max.  Number of clock memoris  Retentivity adjustable  Retentivity preset  Retentivity preset  Raddress area  Number of IO modules  IO address area  Inputs  Size, Max.  Io kbyte  Address area  Inputs  Size, max.  Io kbyte  No  Local data  Per priority class, max.  Io kB per block  Address area  Inputs  Size, Max.  Io kB per block  Address area  Inputs  Size, Max.  Io kB per block  Address area  Inputs  Size, Max.  Io address area  Inputs  Size, Max.  Io address area  Inputs  Size, Max.  Io address area  Io add	counters, flags),	
data area (incl. timers, counters, flags), max.    Size, max.	max.	. , ,
timers, counters, flags), max.  Flag  Size, max.  Number of clock memory bit, grouped into one clock memory byte  Bata blocks  Retentivity adjustable  Retentivity preset No Local data  per priority class, max.  Address area  Number of IO modules  Inputs  Outputs  Outputs  Outputs  Journal of Subyte; max. 32 KB via X1; max. 8 KB via X2 or X4  Coutputs (volume)  Baty byte  Retentivity preset No Local data  4 kbyte; max. 16 KB per block  Address area  Number of IO modules / submodules  Address area  Inputs  Outputs  Journal outputs are in the process image  Abyte; All inputs are in the process image  Abyte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Outputs  Outputs  Batyte  Number of Subprocess images  Number of Subprocess images  Number of Subprocess images  Number of Subprocess Subpr	Extended retentive	
timers, counters, flags), max.  Flag Size, max. Number of clock memories Data blocks Retentivity adjustable Retentivity preset Per priority class, max. Address area Number of IO modules I/O address area Inputs Outputs Per integrated IO subsystem Inputs (volume) Per CM/CP Inputs (volume) Per CM/CP Inputs (volume) Retentivity preset Retentivity preset No Local data Address area Number of IO modules / submodules I/O address area Inputs (volume) Retentivity preset No Local data Address area Inputs (volume) Retentivity preset No Local data Address area Inputs (volume) Retentivity preset No Local data Address area Inputs (volume) Retentivity preset No Local data Address area Inputs (volume) Retentivity preset No Retentive preset preset preset preset preset preset preset preset preset pre	data area (incl.	00 Mb. to Mb. a. using DC 0 0M 04/40/00 V DC LIE
Flag Size, max.   16 kbyte   Number of clock memories   B; 8 clock memory bit, grouped into one clock memory byte   Bata blocks   Retentivity adjustable   Retentivity preset   Cocal data   Per priority class, max.   Mumber of IO   Modules   I/O address area	timers, counters,	20 Mbyte; when using P5 6 0W 24/48/60 V DC HF
Flag Size, max.   16 kbyte   Number of clock memories   B; 8 clock memory bit, grouped into one clock memory byte   Bata blocks   Retentivity adjustable   Retentivity preset   Cocal data   Per priority class, max.   Mumber of IO   Modules   I/O address area		
■ Size, max.   16 kbyte  Number of clock memories   8; 8 clock memory bit, grouped into one clock memory byte   Data blocks   Retentivity adjustable   Yes    Retentivity preset   No   Local data   64 kbyte; max. 16 KB per block   Address area   16 384; max. number of modules / submodules   I/O address area   16 384; max. number of modules / submodules   I/O address area   32 kbyte; All inputs are in the process image   Dutputs   32 kbyte; All outputs are in the process image   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Dutputs   34 kbyte; max. 34 kbyte   Dutputs   35 kbyte; max. 35 kbyte   Dutputs   36 kbyte   Dutputs   37 kbyte   Dutputs   38 kb		
Number of clock memories  Data blocks  Retentivity adjustable  Retentivity preset   No   Local data  per priority class, max.  Address area  Number of IO modules  Inputs  Outputs  Inputs  Jack byte; All inputs are in the process image  per integrated IO subsystem  Inputs (volume)  Inputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Outputs  Jack byte; max. 32 KB via X1; max. 8 KB via X2 or X4  Number of subprocess images  Number of subprocess images  Number of subprocess images  Number of subprocess images		16 khyte
Big 8 clock memory bit, grouped into one clock memory byte  Data blocks Retentivity adjustable Retentivity preset Retentive presetentive prese		
Data blocks  Retentivity adjustable  Retentivity preset No Local data  per priority class, max.  Address area  Number of IO modules  Io address area  Number of IO address area  Inputs  Outputs  Per integrated IO subsystem  Inputs (volume)  Per CM/CP  Inputs (volume)  Subyte; max. 32 KB via X1; max. 8 KB via X2 or X4 (volume)  Per CM/CP  Inputs (volume)  Retentivity adjustable  Yes  No Local data  Fer Bolock  Address area  Address area  Inputs (volume)  Bubyte; All inputs are in the process image  Per integrated IO subsystem  Inputs (volume)  Bubyte; max. 32 KB via X1; max. 8 KB via X2 or X4 (volume)  Per CM/CP  Inputs (volume)  Bubyte  Bubyte  Subprocess images  Number of subprocess images  Number of subprocess images  Number of subprocess images  Number of subprocess images		8; 8 clock memory bit, grouped into one clock memory byte
<ul> <li>Retentivity adjustable</li> <li>Retentivity preset No</li> <li>Local data</li> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>Inputs</li> <li>Inputs</li> <li>Inputs (volume)</li> <li>Per integrated IO subsystem</li> <li>Inputs (volume)</li> <li>B kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>B kbyte</li> <li>Outputs (volume)</li> <li>Subsyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>B kbyte</li> <li>Outputs (volume)</li> <li>Subprocess images</li> <li>Number of subprocess images</li> <li>Number of subprocess images</li> </ul>		
adjustable   Yes   No   Local data   Per priority class, max.   64 kbyte; max. 16 KB per block   Address area   Number of IO   16 384; max. number of modules   submodules   I/O address area   Inputs   32 kbyte; All inputs are in the process image   Outputs   32 kbyte; All outputs are in the process image   Per integrated IO subsystem   Inputs (volume)   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Outputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Outputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Outputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Outputs   32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4   Outputs   34 kbyte; max. 35 kbyte   Outputs   35 kbyte; max. 36 kbyte   Outputs   36 kbyte   Outputs   37 kbyte   Outputs   38 kbyte   Output		
<ul> <li>Retentivity preset</li> <li>Local data</li> <li>per priority class, max.</li> <li>64 kbyte; max. 16 KB per block</li> <li>Address area</li> <li>Number of IO modules</li> <li>I6 384; max. number of modules / submodules</li> <li>I/O address area</li> <li>Inputs</li> <li>32 kbyte; All inputs are in the process image</li> <li>Outputs</li> <li>32 kbyte; All outputs are in the process image</li> <li>per integrated IO subsystem</li> <li>Inputs (volume)</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>Per CM/CP</li> <li>Inputs (volume)</li> <li>8 kbyte</li> <li>Outputs (volume)</li> <li>8 kbyte</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> </ul>		Yes
Local data  • per priority class, max.  Address area  Number of IO modules  I/O address area  • Inputs  • Outputs  — Inputs (volume)  — Inputs (volume)  B kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4 (volume)  — Outputs  — Outputs  — Outputs  — Outputs  — Outputs  — Outputs  — Inputs (volume)  B kbyte  — Outputs  — Number of subprocess images  • Number of subprocess  — Name A KB via X 1; max. 8 KB via X2 or X4  — Outputs  — Number of subprocess  — Number of subprocess  — Name A KB via X2  — Outputs  — O		
<ul> <li>per priority class, max.</li> <li>Address area</li> <li>Number of IO modules</li> <li>I6 384; max. number of modules / submodules</li> <li>I/O address area</li> <li>Inputs</li> <li>32 kbyte; All inputs are in the process image</li> <li>Outputs</li> <li>Outputs (volume)</li> <li>J2 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>J2 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>B kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>B kbyte</li> <li>Outputs (volume)</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> </ul>		No
Max.  Address area  Number of IO modules  Io address area  Io address area  Inputs  Inputs  Io address area  Inputs  Io address area  Io linputs  Io address area  I		
Address area  Number of IO modules    16 384; max. number of modules / submodules     16 384; max. number of modules / submodules     16 384; max. number of modules / submodules     18 32 kbyte; All inputs are in the process image     19 0 0utputs     19 0 0utputs     10 0 0utp	<ul><li>per priority class,</li></ul>	64 khyte: may 16 KB ner block
Number of IO modules  I/O address area  Inputs  Outputs  Inputs  Input	max.	by hoyte, max. To NB per block
modules    16 384; max. number of modules / submodules     17	Address area	
VO address area	Number of IO	1C 004: may recomb as of mandy land / as beneated as
<ul> <li>Inputs</li> <li>32 kbyte; All inputs are in the process image</li> <li>Outputs</li> <li>32 kbyte; All outputs are in the process image</li> <li>per integrated IO subsystem</li> <li>Inputs (volume)</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>Per CM/CP</li> <li>Inputs (volume)</li> <li>8 kbyte</li> <li>Outputs (volume)</li> <li>8 kbyte</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> </ul>	modules	16 384; max. number of modules / submodules
<ul> <li>Inputs</li> <li>32 kbyte; All inputs are in the process image</li> <li>Outputs</li> <li>32 kbyte; All outputs are in the process image</li> <li>per integrated IO subsystem</li> <li>Inputs (volume)</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> <li>Outputs (volume)</li> <li>Per CM/CP</li> <li>Inputs (volume)</li> <li>8 kbyte</li> <li>Outputs (volume)</li> <li>8 kbyte</li> <li>Subprocess images</li> <li>Number of subprocess images, max.</li> <li>32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4</li> </ul>	I/O address area	'
Outputs 32 kbyte; All outputs are in the process image per integrated IO subsystem  — Inputs (volume) 32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4  — Outputs (volume)  Per CM/CP — Inputs (volume) 8 kbyte — Outputs (volume) 8 kbyte  Subprocess images  Number of subprocess images  32 kmyte; max. 32 KB via X1; max. 8 KB via X2 or X4  8 kbyte  34 kbyte		32 kbyte: All inputs are in the process image
per integrated IO subsystem — Inputs (volume) 32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4 — Outputs (volume) 32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4  per CM/CP — Inputs (volume) 8 kbyte — Outputs (volume) 8 kbyte  Subprocess images  Number of subprocess images, max.  32 KB via X1; max. 8 KB via X2 or X4  8 kbyte  34 kbyte		
— Inputs (volume)  — Outputs (volume)  per CM/CP — Inputs (volume)  B kbyte  — Outputs (volume)  8 kbyte  — Outputs (volume)  8 kbyte  — Outputs (volume)  8 kbyte  Subprocess images  Number of subprocess images, max.		
Outputs (volume)  per CM/CP  Inputs (volume)  8 kbyte  Outputs (volume)  8 kbyte  Number of subprocess images, max.  32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4  8 kbyte  8 kbyte  32 kbyte; max. 32 KB via X1; max. 8 KB via X2 or X4		
(volume)  per CM/CP  — Inputs (volume) 8 kbyte  — Outputs (volume) 8 kbyte  Subprocess images  ● Number of subprocess images, max.		pz kuyte, max. sz nd via n i; max. s nb via xz or x4
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— Inputs (volume) 8 kbyte  — Outputs (volume) 8 kbyte  Subprocess images  Number of subprocess 32 images, max.	l, ,	
Outputs (volume)  Subprocess images  Number of subprocess images, max.  8 kbyte	•	
(volume) Subprocess images  ● Number of subprocess 32 images, max.		8 kbyte
Subprocess images  Number of subprocess images, max.		8 khyte
Number of subprocess 32 images, max.	(volume)	o noyto
Number of subprocess 32 images, max.	Subprocess images	
subprocess 32 images, max.		
images, max.	subprocess	32
•	images, max.	
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Number of distributed IO systems	64; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link)	
Number of DP mas	ters	
<ul><li>integrated</li></ul>	1	
● Via CM	8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total	
Number of IO Conti	rollers	
<ul><li>integrated</li></ul>	2	
● Via CM	8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total	
Rack	•	
<ul> <li>Modules per rack, max.</li> </ul>	32; CPU + 31 modules	
<ul> <li>Number of lines, max.</li> </ul>	1	
PtP CM		
<ul><li>Number of PtP CMs</li></ul>	the number of connectable PtP CMs is only limited by the number of available slots	
Time of day		
Clock		
• Туре	Hardware clock	
<ul> <li>Backup time</li> </ul>	6 wk; At 40 °C ambient temperature, typically	
<ul><li>Deviation per day, max.</li></ul>	10 s; Typ.: 2 s	
Operating hours counter		
<ul><li>Number</li></ul>	16	
	•	

Dimensions	
Width	175 mm
Height	147 mm
Depth	129 mm
Weights	•
Weight, approx.	2 079 g



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